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ME 03-002

July 9, 2004

To: Mail Stop Application Number
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: Stephen B. Ackerman

Subject: Serial No.: 10/802,566
Filing Date: 03/17/2004
Inventor: M. S. Lin
Title: High Performance IC Chip Having Discrete Decoupling
Capacitors Attached to its IC Surface

REQUEST TO CORRECT FILING RECEIPT

Dear Sir:

On the copy of the enclosed Filing Receipt, there is an error in the list of Applicants. The Applicant's residence is misspelled. Mr. Lin's name and corrected residence reads;
Mou-Shiung Lin, Hsin-Chu, Taiwan.

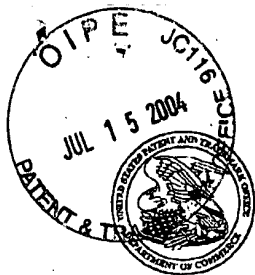
Please make the necessary change to correct the error on the Filing Receipt.

If there are any questions, please contact the undersigned attorney at (845) 452-5863. Thank you for your attention to this matter.

Respectfully Submitted,

Stephen B. Ackerman, Reg. No. 37,761

JUN. 04 2004



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/802,566	03/17/2004	2811	1043	MEG03-002	5	74	7

CONFIRMATION NO. 3507

STEPHEN B. ACKERMAN
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 POUGHKEEPSIE, NY 12603

FILING RECEIPT



OC000000012831982

Date Mailed: 06/01/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Chn
 Mou-Shiung Lin, Hsin-Chil City, TAIWAN;

Assignment For Published Patent Application

Megic Corporation;

Domestic Priority data as claimed by applicant

This appln claims benefit of 60/455,154 03/17/2003

Foreign Applications

If Required, Foreign Filing License Granted: 05/31/2004

Projected Publication Date: 09/23/2004

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

High performance IC chip having discrete decoupling capacitors attached to its IC surface

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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